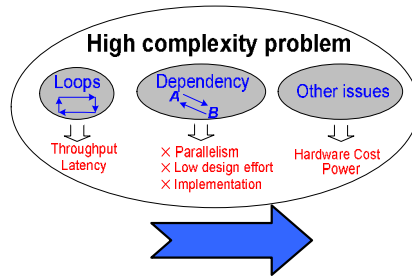


LOW COMPLEXITY HARDWARE ORIENTED H.264/AVC MOTION ESTIMATION ALGORITHM AND RELATED LOW COST AND LOW POWER ARCHITECTURE DESIGN

博士課程修了 黄異青

研究背景

- I Videos everywhere → compression technique
- I Complexity problem → speedup algorithm
- I Impact of pipeline → hardware dataflow
- I Expansion of image size → throughput issue



Target:

- ∅ High resolution image
- ∅ Hardware oriented fast algorithm
- ∅ Low cost architecture (hardware, power, design effort)

研究内容

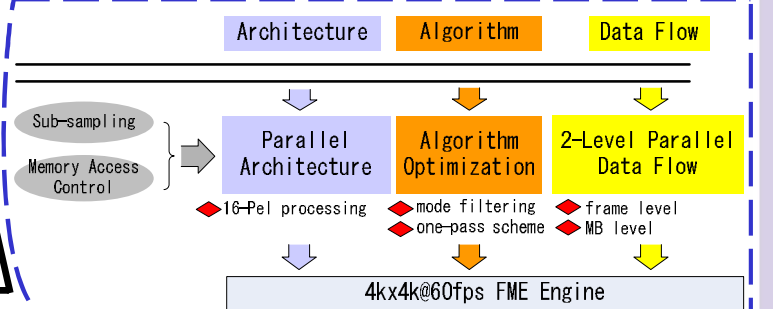
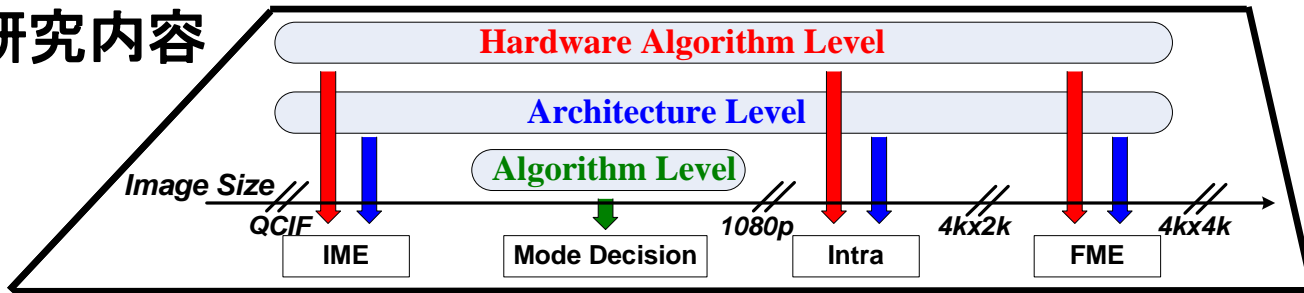
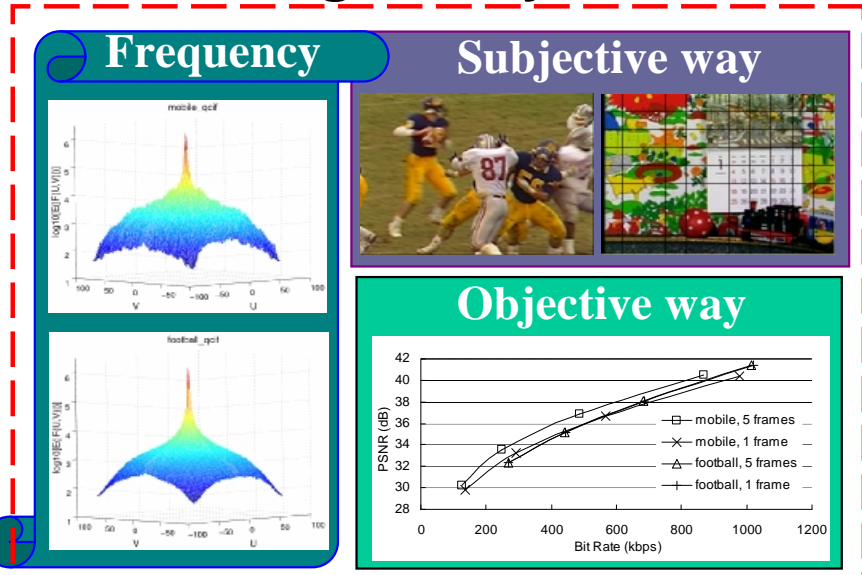
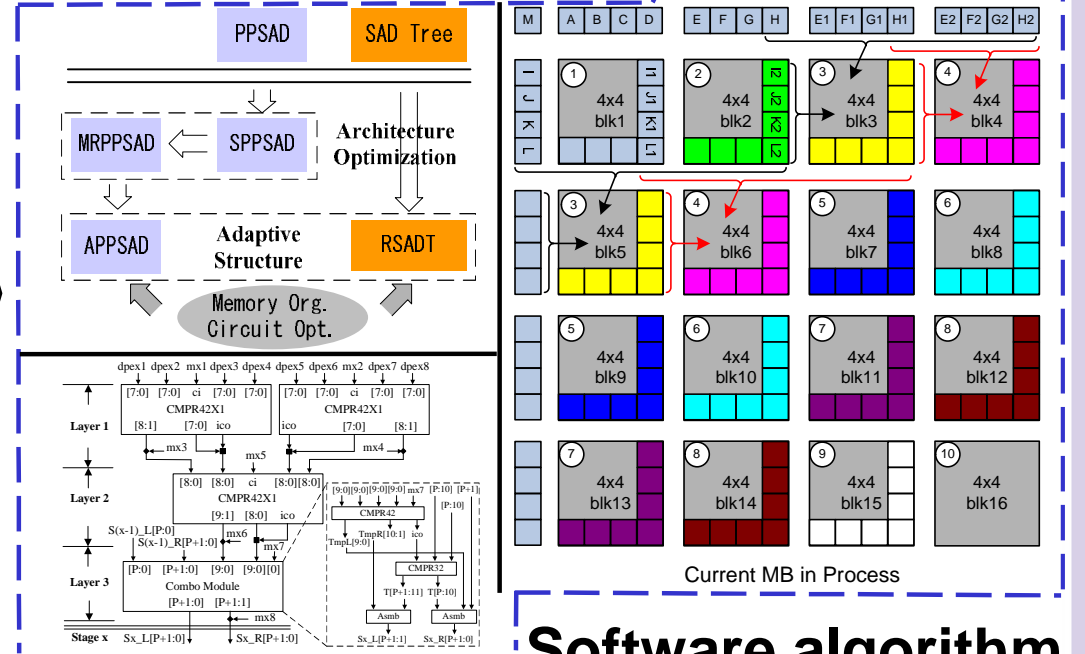


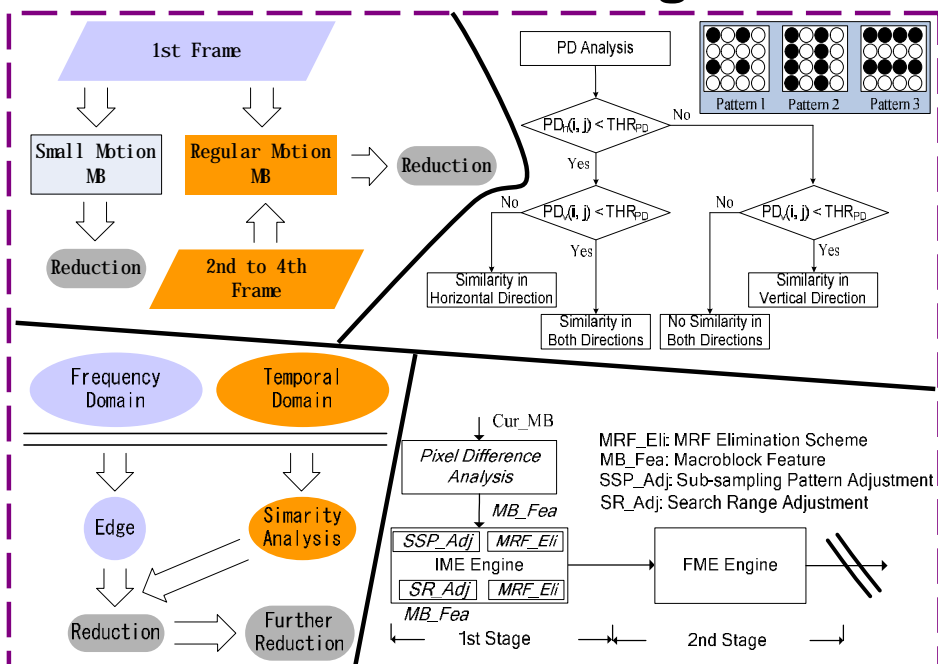
Image analysis



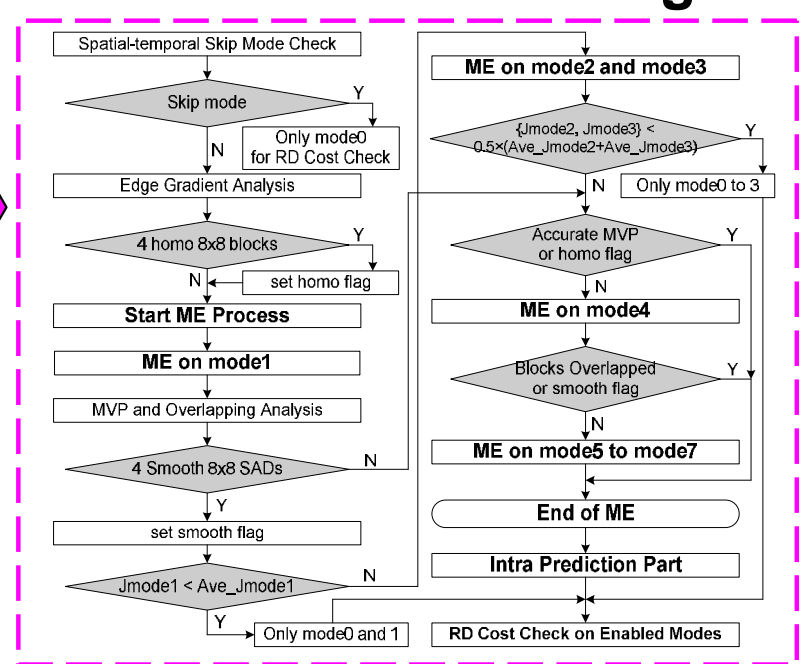
Hardware architecture



Hardware oriented algorithm



Software algorithm



主な発表論文

- p 8 IEICE Journals, 1 IPSJ Journal, 1 SCI Indexed Chinese Journal
- p 30 International conference papers, including ICIP, MMSP, ICME, MMM, EUSIPCO, DSP, GLSVLSI, ISCAS, APCCAS, ASICON, ISOCC, etc



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